Fig. 1

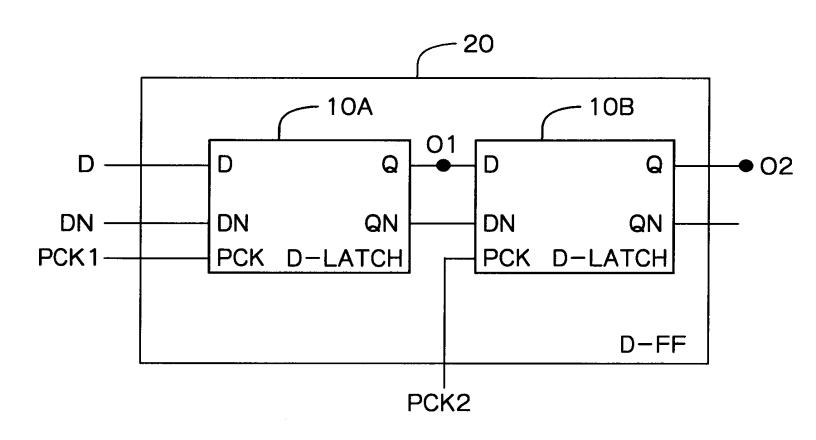
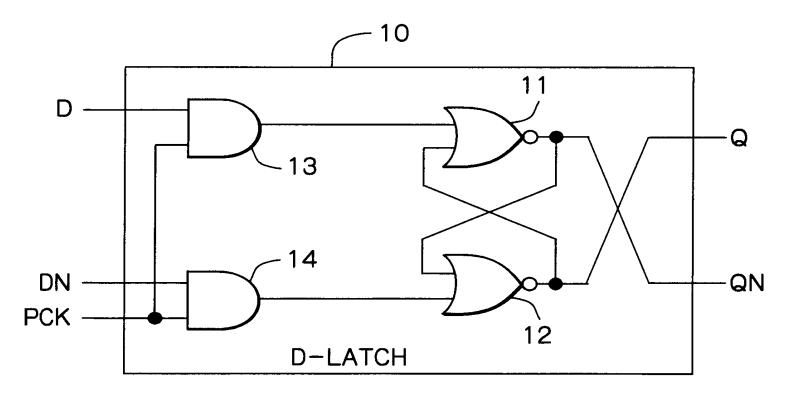


Fig. 2A



**P-CHANNEL** N-CHANNEL m ပ 4 Fig. 2B A B ပ်

 $\Box$ 

Fig. 2C

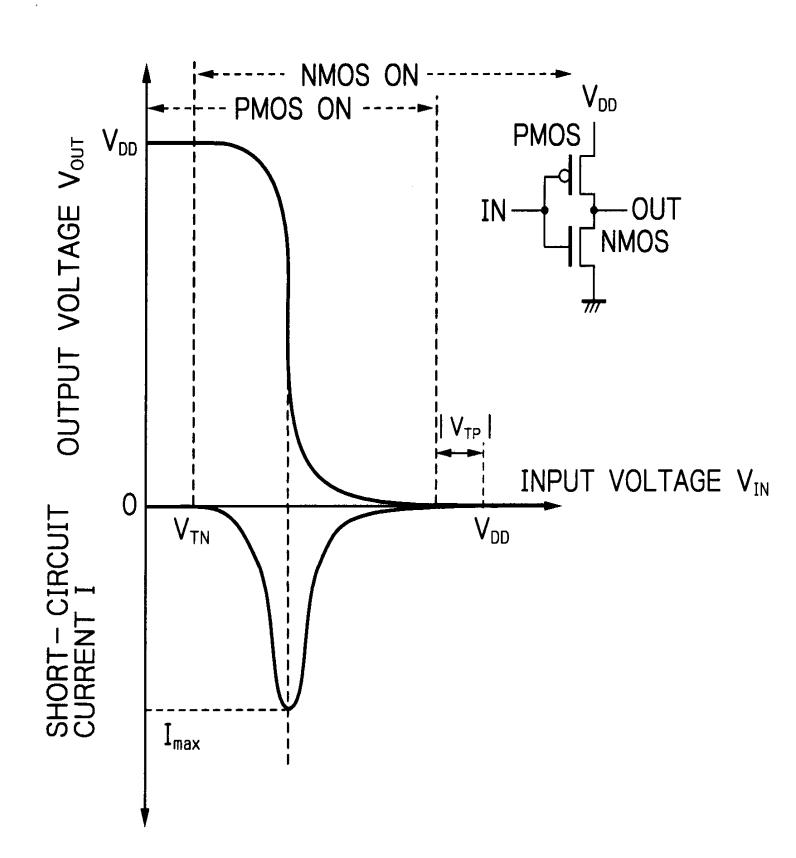


Fig. 3

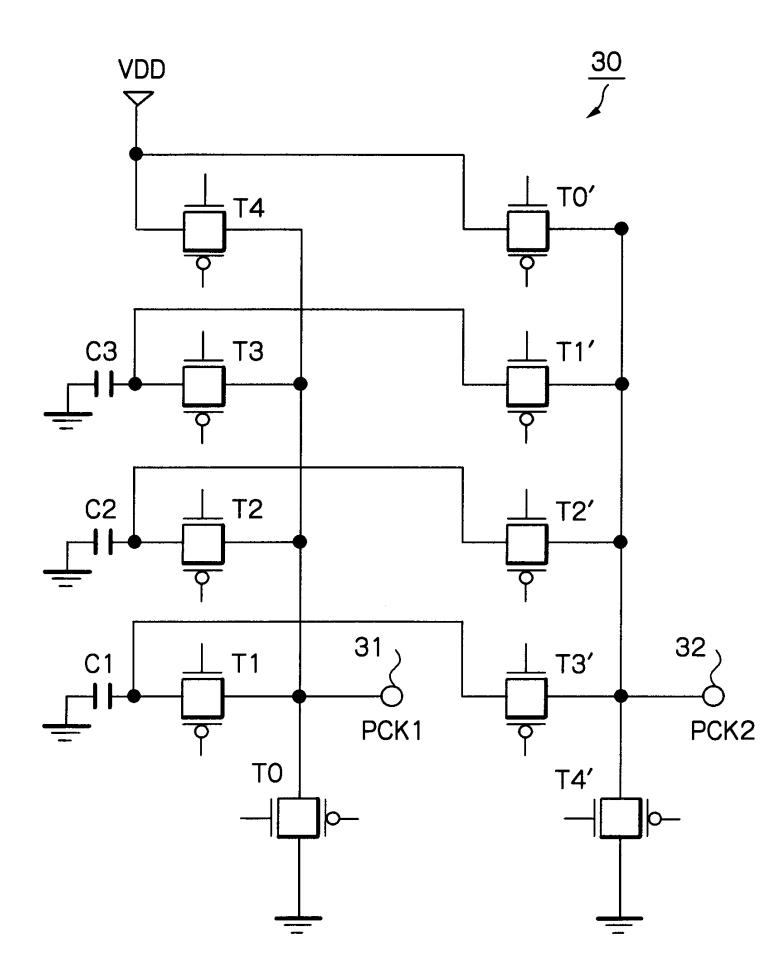


Fig. 4

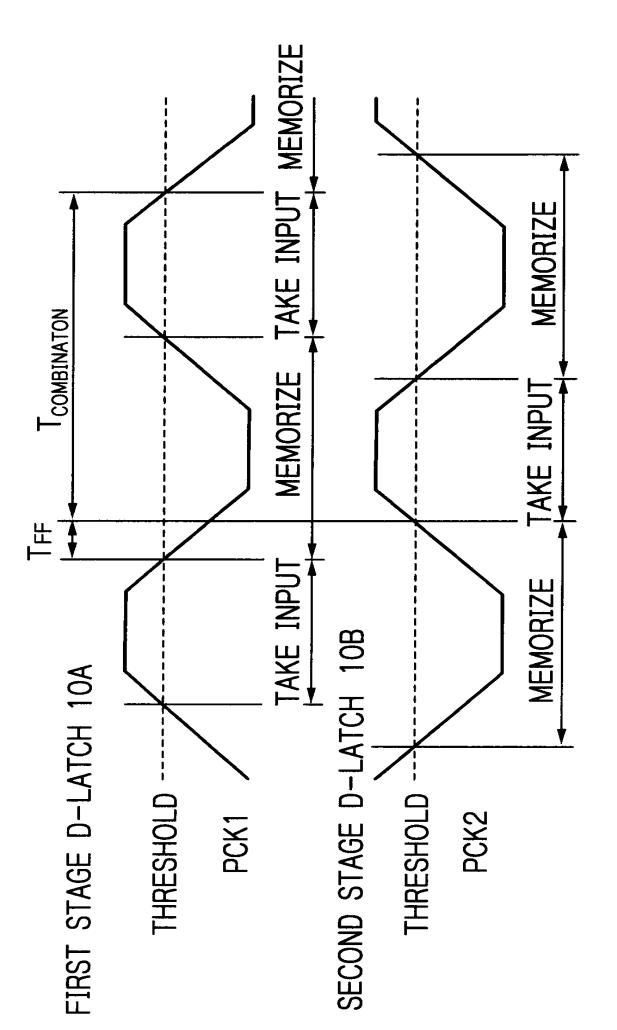


Fig. 5

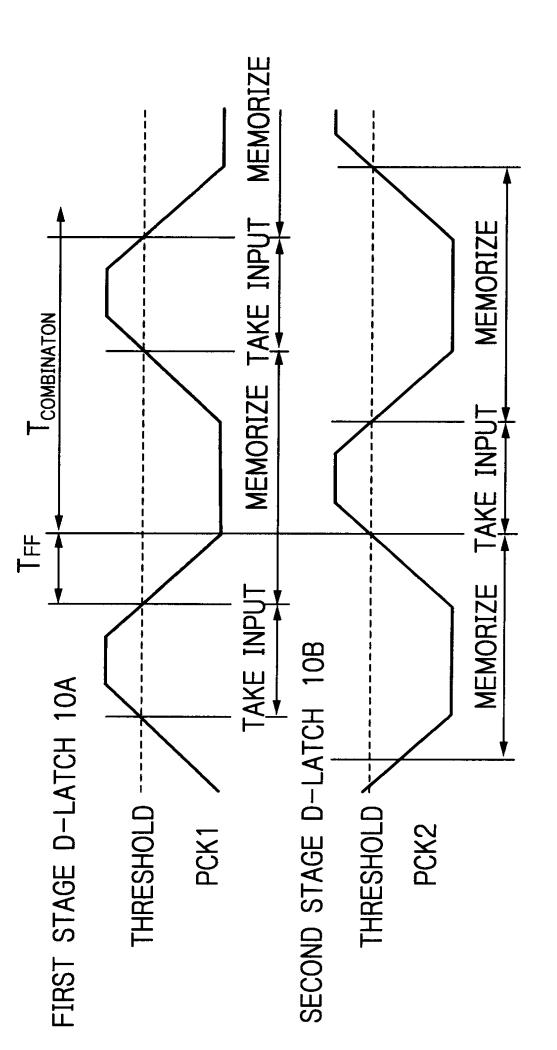
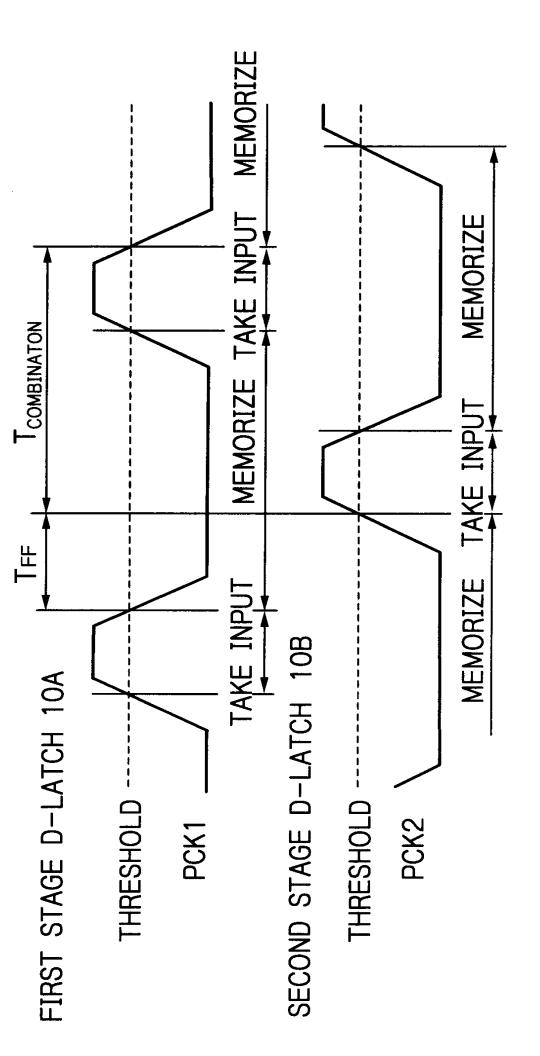


Fig. 6



PCK2\_ PCK1 02 0

Fig. 7

Fig. 8

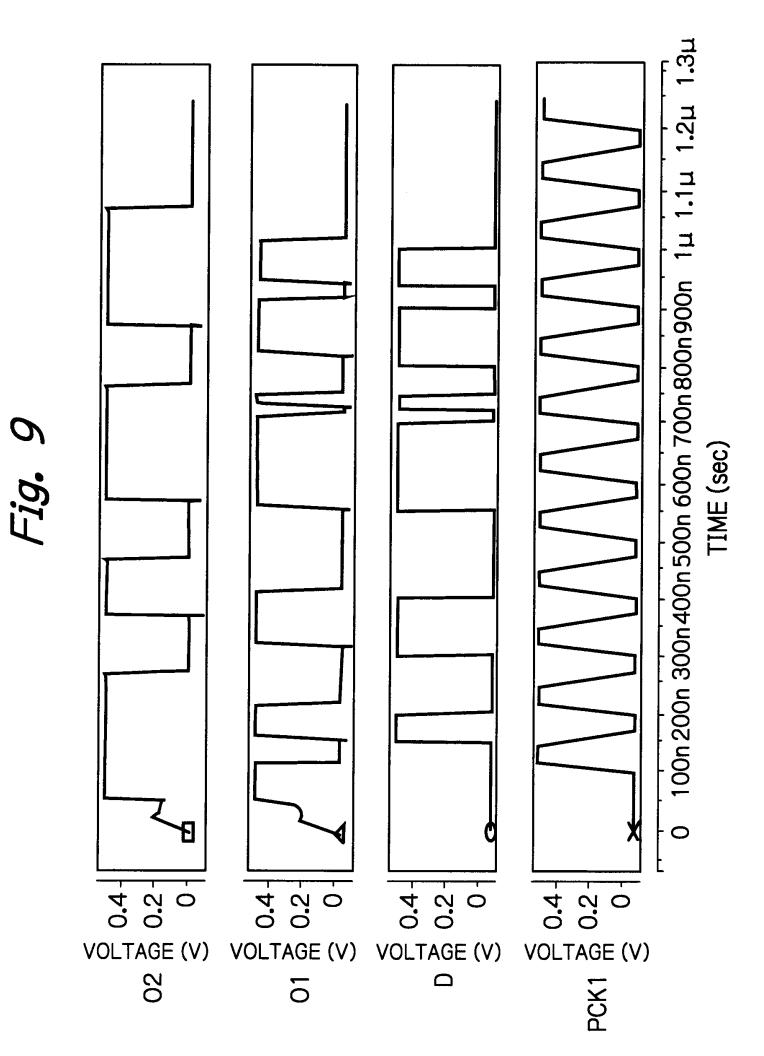
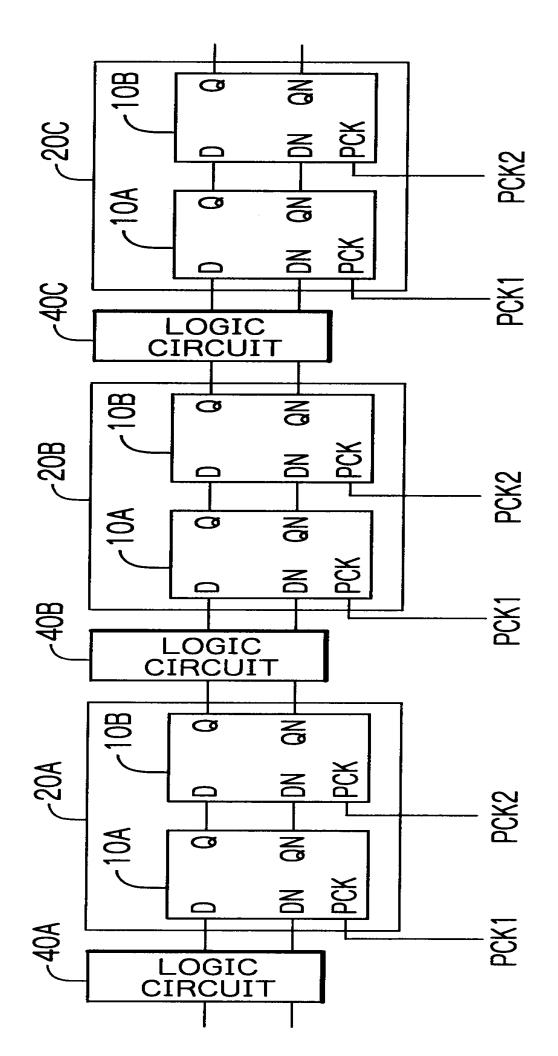
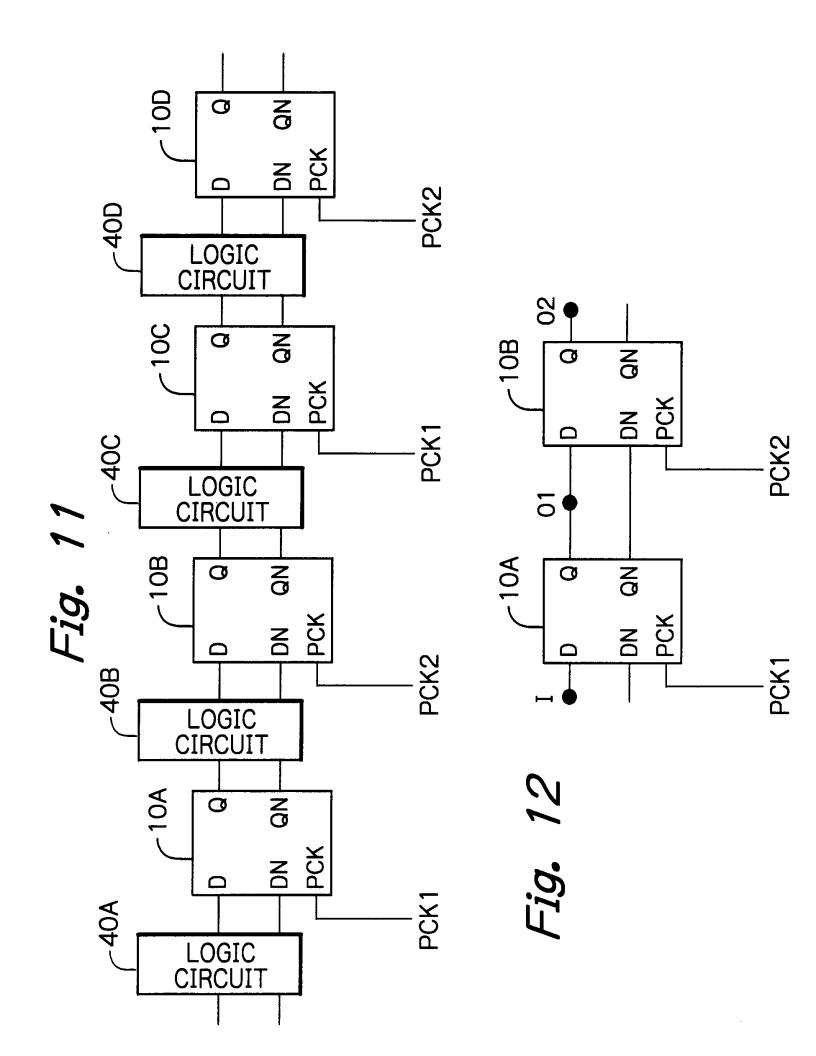


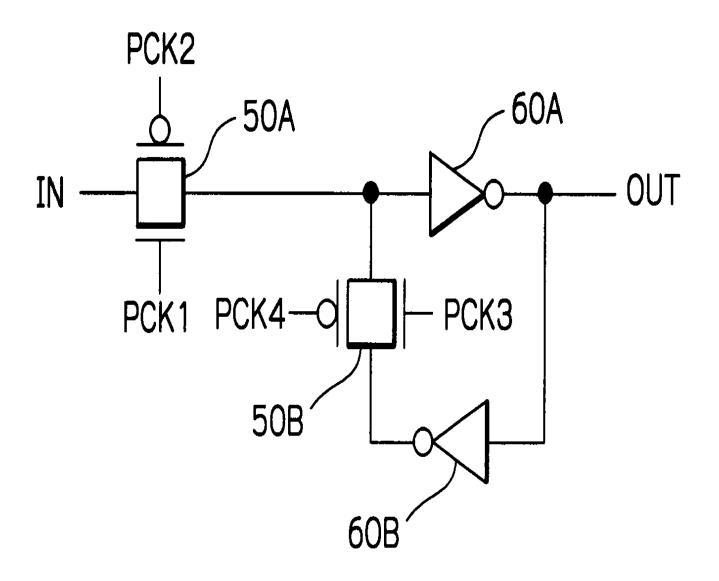
Fig. 10





THRESHOLD TAKE INPUT , THRESHOLD MEMORIZE MEMORIZE TAKE INPUT MEMORIZE Fig. 13 TAKE INPUT MEMORIZE TAKE INPUT PCK2 PCK1 02 0

Fig. 14



TAKE INPUT - TIME MEMORIZE TAKE INPUT THRESHOLD **THRESHOLD** MEMORIZE THRESHOLD THRESHOLD TAKE INPUT MEMORIZE TAKE INPUT MEMORIZE Fig. 15 MEMORIZE TAKE INPUT PCK2 **PCK3** PCK4 PCK1

Fig. 16

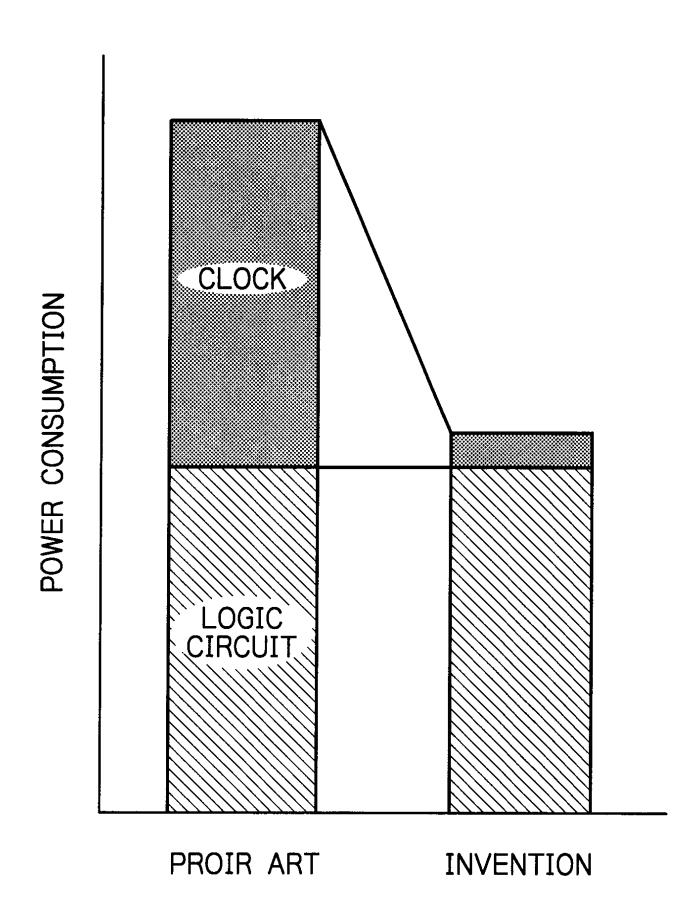


Fig. 17 PRIOR ART

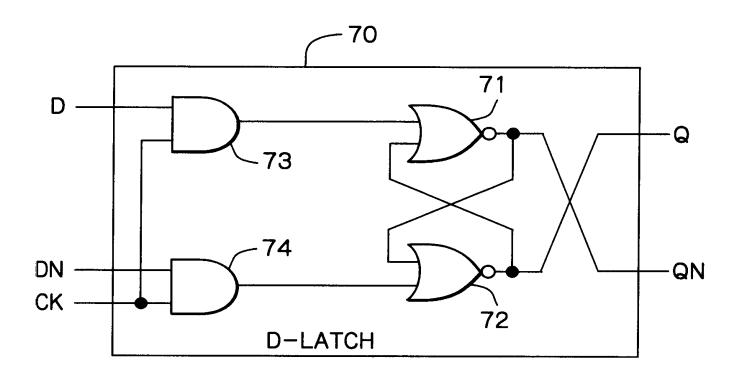
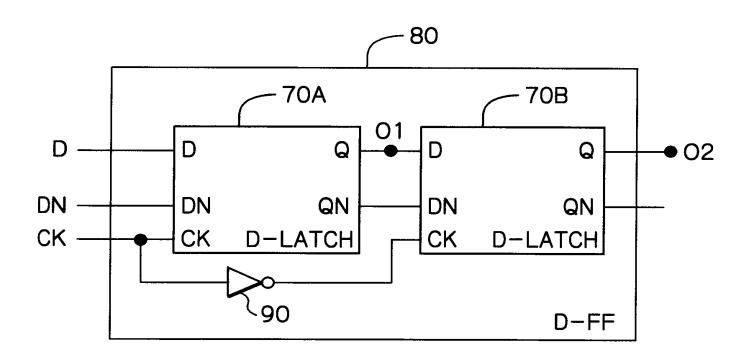


Fig. 18 PRIOR ART



## Fig. 19 PRIOR ART

